

Notice of References Cited

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| 09/620,474 | Reexamination | rí VAKOTO |
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U.S. PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Name | Classification |
|---|---|--|-----------------|------|----------------|
| | Α | US- | | | |
| | В | US- | | | |
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| | J | US- | | | |
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FOREIGN PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Country | Name | Classification |
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| | s | | | | | |
| | Т | | | | | |

NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|--|
| | U | Amadori, S. et al. "Design of Complex Systems with a VHDL Based Methodology". Proc. European Design Automation Conf. 1992. pp.658-663. |
| | V | Hopper, A. et al. "Multiple vs. Wide Shared Bus Multiprocessors." Int'l Conf. on Computer Architecture, 1989. pp.300-306. |
| | w | Wunder, B. et al. "A New Concept for Accurate Modeling of VLSI Interconnections and its Application for Timing Simulation", Proc. Euro. DAC. 1996. pp.72-77. |
| | x | Wear, L. "A Comparison of Methods for Simulating Computer Bus Architectures", Proc. 13 th Conf. on Winter Simulation. 1981. pp.353-359. |

A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.



Notice of References Cited

Application/Control No.

O9/620,474

Examiner

Ayal I Sharon

Applicant(s)/Patent Under
Reexamination
FUJIWARA, MAKOTO

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U.S. PATENT DOCUMENTS

| | | Document Number | Date | U.S. I ATENT BOOMENTO | |
|---|---|-------------------------------|---------|-----------------------|----------------|
| * | | Country Code-Number-Kind Code | MM-YYYY | Name | Classification |
| | Α | US- | | | |
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FOREIGN PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Country | Name | Classification |
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| | Т | | | | | |

NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|--|
| | U | Langley, F. "Simulating Modular Microcomputers". Proc. of the 11 th Annual Simulation Symposium. 1978. pp.319-342. |
| | ٧ | Hollingsworth, J. "The Integration of Application and System Based Metrics in a Parallel Program Performance Tool". 1991. ACM 0-89791-390-6/91/0004/0189 |
| | V | Komatsu, T. et al. "Simulator for the Evaluation of Distributed Network-System Performance". Proc. of the 1996 Winter Sim. Conf. 1996. |
| | x | |

A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

*Dates in MM-YYYYY format are publication dates. Classifications may be US or foreign.

